

A1  
conc.

5. A circuit arrangement (100) as claimed in claim 1, characterized in that the material of the dielectric shielding layer (30; 35) is also opaque.

6. A circuit arrangement (100) as claimed in claim 1, characterized in that the signal-generating unit (40) comprises at least one oscillator circuit consisting of at least one capacitive unit, particularly a capacitor, and at least one resistive unit, particularly a resistor, and/or at least one oscillator circuit consisting of at least one capacitive unit, particularly a capacitor, and at least one inductive unit, particularly a coil.

7. A circuit arrangement (100) as claimed in claim 1, characterized in that at least an evaluation unit (70), particularly at least a differential evaluation unit is constituted by the first counting unit (50), the second counting unit (55) and the comparator unit (60).

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9. A circuit arrangement (100) as claimed in claim 7, characterized in that the evaluation unit (70) generates the error indication when the actual value deviates from the nominal range.

10. A circuit arrangement (100) as claimed in claim 1, characterized in that the first counting unit (50) and/or the second counting unit (55) is formed on a digital basis.

11. A card, particularly a chip card or smart card, comprising at least an electric or electronic circuit arrangement (100) as claimed in claim 1.

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A3  
15. A method as claimed in claim 13, characterized in that the error indication is generated in the evaluation unit (70) when the actual value deviates from the nominal range.

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